



*11<sup>th</sup> International Symposium on POWER electronics – Ee 2001*  
Novi Sad, Yugoslavia, Oct. 31 – Nov. 2, 2001

<b>T2-3</b>		<b>EFFICIENCY OF DRIVES EFIKASNOST POGONA</b>	
<b>Hall 2</b>		<b>THURSDAY – ČETVRTAK Nov. 1, 2001 – 01. 11. 2001.</b>	<b>15<sup>00</sup> h</b>
<b>Chairman:</b>		<b>Prof. dr Miloš Živanov YU</b>	
<b>T2-3.1</b>	D. Marčetić S. Vukosavić	Italy YU	Model vektorski upravljano pogona za male signale SMALL SIGNAL MODEL OF VECTOR CONTROLLED DRIVES
<b>T2-3.2</b>	B. Blanuša S. Vukosavić	R. S., B&H YU	Algoritam za minimizaciju snage gubitaka vektorski regulisanog asinhronog pogona zasnovan na primjeni fazi logike AN ALGORITHM FOR POWER LOSSES MINIMIZATION OF VECTOR CONTROLLED AC DRIVES BASED ON FUZZY LOGIC